

Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) A reference mark detector for use with a metrological scale or encoder having a patterned reference mark movable relative to the reference mark ~~detector~~ detector, the detector ~~comprising~~ comprising:
_____ a detector array for detecting the patterned reference mark, the detector array comprising at least two sets of detector ~~elements~~ elements, each set being formed as a pattern which relates to the pattern of the reference ~~mark~~ mark, each detector element of each set of detector elements having an output;
_____ a summer, for summing each of the outputs from each of the elements of a set with the other elements of the same set; and
_____ a subtractor, for subtracting the summed output from a second of the sets from the summed output of a first of the sets.
2. (Canceled)
3. (Currently Amended) A reference mark detector as claimed in ~~claim 2~~ claim 1, wherein the pattern of the first and second set of detectors is ~~irregular~~ non-periodic.
4. (Previously Presented) A reference mark detector as claimed in claim 1, wherein the detector array comprises two rows of elements, the first of the rows containing a first one of the sets of detector elements and the second row containing a second one of the sets of detector elements.
5. (Currently Amended) A reference mark detector as claimed in ~~claim 4~~ claim 4, wherein the first of the rows is displaced relative to the second of the rows in a direction of movement of the reference mark detector relative to the reference mark in use.

6. (Currently Amended) A reference mark detector as claimed in ~~claim 1~~ claim 1, wherein the detector array comprises a single row, a first set of detector elements and a second set of detector elements comprising detector elements in the row.

7. (Currently Amended) A reference mark detector as claimed in ~~claim 6~~ claim 6, wherein the first set of detector elements are connected together to provide a distinct output when the reference mark is detected in a first position and the second set are connected together to provide a distinct output when the reference mark is detected at a position different to the first position.

8. (Currently Amended) A reference mark detector as claimed in ~~claim 7~~ claim 7, wherein the position of the first and second set of detector elements correspond to positions in a notional table, the first row of the table containing high or low values corresponding to the pattern of the reference mark, the second row of the table containing the same values, the values in the second row being displaced relative to the position in the table of the values of the first row in the direction of the rows, each value from the second row being deducted from the values of the first row immediately above the values of the second row to form a resultant for each position in the table, the position in the table of the positive high values of each resultant representing the positions of the first set of detector elements and the positions in the table of the negative high values of each resultant representing the positions of second set of detector elements.

9. (Currently Amended) A reference mark detector as claimed in ~~claim 3~~ claim 3, wherein the summing of the outputs and the subtraction of the sums is done digitally.

10. (Currently Amended) A metrological scale ~~or encoder having periodic markings for incremental measurement operations~~ and a scale reader for ~~determining the~~ determining a displacement of the reader relative to the scale by reading the periodic markings scale, comprising:

~~_____the scale including a metrological scale having periodic markings for~~
~~incremental measurement and including a patterned reference mark,~~
~~_____the scale reader including a reference mark detector including a detector array,~~
~~the array comprising at least two sets of detector elements, and~~
~~_____each set of the detector elements having separately processed outputs-outputs;~~
~~and including means~~
~~_____a circuitry for producing a signal from the outputs of the elements-elements,~~
~~the signal having a value which crosses zero when the reference mark is detected by the~~
~~reference mark detector.~~

11. (Currently Amended) A metrological scale or encoder as claimed in ~~claim 10~~
claim 10, wherein the means for producing the signal includes summing the outputs of each
of the elements in a first set of the said at least two sets to produce a first summed output and
summing the output from a second set of the said at least two sets to produce a second
summed output, then subtracting the second summed output from the first summed output.

12. (Currently Amended) A metrological scale or encoder as claimed in ~~claim 10~~
claim 10, wherein the array is formed as at least two rows and a first one of the said sets is
included in one of the rows, and a second set is included in an other of the two rows.

13. (Currently Amended) A metrological scale or encoder as claimed in ~~claim 10~~
claim 10, wherein the array is formed as a single row and a first and second set of sets of
elements are comprised in the single row.

14. (Currently Amended) A metrological scale as claimed in ~~claim 10~~ claim 10,
wherein the reference mark comprises bits missing or added to the periodic markings of the
scale or encoder.